250738US2CONT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Yoshinori OKUMURA, et al.

SERIAL NO: New CONT Application

GAU:

FILED:

Herewith

**EXAMINER:** 

FOR:

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

# INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

#### REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references were provided in previously filed U.S. Application No. 10/444,959, filed May 27, 2003.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

#### RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

## CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

# **DEPOSIT ACCOUNT**

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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Katherine D. Pauley Registration No. 50,607

Form PTO 1449 (Modified)		U.S. DEPARTMENT PATENT AND TRAI	OF COMMERCE	ATTY DOCKET NO.		SERIAL NO.				
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LIST OF	REFE	RENCES CITED BY AP	PLICANT	APPLICANT Yoshinori OKUMURA, et al.						
				FILING DATE GROUP						
				Herewith		GROUP				
U.S. PATENT DOCUMENTS										
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE			
	AA	5,656,841	08/1997	Watanabe, et al.						
	AB				1					
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FOREIGN PATENT DOCUMENTS										
		DOCUMENT NUMBER	DATE	COUNTRY	COUNTRY			TRANSLATION YES NO		
	AO	2000-353803	12/19/2000	Japan (w/Enlish Abstract)	x					
	AP	2002-231821	08/16/2002	Japan (w/English Abstract, corr. USSN			x			
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)										
	AW	K. IMAI, et al., IEDM, pages 455-458, "CMOS DEVICE OPTIMIZATION FOR SYSTEM-ON-A-CHIP APPLICATIONS," 2000								
	AX	H. WATANABE, et al., IEDM, pages 975-978, "NOVEL 0.44µm² Ti-SALICIDE STI CELL TECHNOLOGY FOR HIGH-DENSITY NOR FLASH MEMORIES AND PERFORMANCE EMEDDED APPLICATION," 1998								
	AY									
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	AZ			Additional References sheet(s) attached						
Examiner					Date Considered					
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.										
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